## LB1937T

Monolithic Digital IC

## 1-2 Phase Excitation Stepping Motor Driver

ON Semiconductor ${ }^{\text {® }}$
http://onsemi.com

## Overview

The LB1937T is a 2-phase bipolar drive stepping motor driver IC that supports low-voltage drive and can drive two stepping motors at the same time. The LB1937T's miniature package and minimal number of external components reduces the required mounting area. It also provides high-efficiency motor drive and can reduce circuit current consumption. Since it provides a current detection pin and supports PWM control input, it can be used to implement current chopper control at the system level. The LB1937T is optimal for the stepping motors used for lens drive in digital cameras, printers, and movie cameras.

## Functions and Features

- Low saturation voltage forward/reverse motor driver $\left(\mathrm{V}_{\mathrm{O}}\right.$ sat $=0.3 \mathrm{~V}$ at $\left.\mathrm{I}_{\mathrm{O}}=200 \mathrm{~mA}\right)$
- Four H-bridge channels
- Wide usable voltage range (Allowable voltage range: 2.5 V to 9.5 V , absolute maximum rating: 10.5 V )
- Supports PWM input (Low power consumption can be achieved in slow delay mode that uses IN1/IN2 = H/H logic.)
- Motor (coil) current detection pin
- Built-in thermal shutdown circuit
- Thin form factor miniature package (TSSOP24)


## Specifications

Absolute Maximum Ratings at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Maximum supply voltage | $\mathrm{V}_{\text {CC }}$ max |  | -0.3 to +10.5 | V |
|  | VS max |  | -0.3 to +10.5 | V |
| Maximum output voltage | $V_{\text {OUT }}$ max |  | VS+ VSF | V |
| Input voltage | $\mathrm{V}_{\text {IN }}$ max |  | -0.3 to +8.0 | $\checkmark$ |
| Ground pin source current | IGND max | Per channel | 800 | mA |
| Allowable power dissipation | Pd max | When mounted on a circuit board * | 1100 | mW |
| Operating temperature | Topr |  | -30 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | Tstg |  | -40 to +150 | ${ }^{\circ} \mathrm{C}$ |

* Specified circuit board : $114.3 \times 76.1 \times 1.6 \mathrm{~mm}^{3}$, glass epoxy

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Allowable Operating Range at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Supply voltage | $\mathrm{V}_{\mathrm{CC}}$ |  | 2.5 to 9.5 | V |
|  | VS |  | 2.5 to 9.5 |  |
| High-level input voltage | $\mathrm{V}_{\mathrm{IH}}$ |  | 2.0 to 7.5 | V |
| Low-level input voltage | $\mathrm{V}_{\mathrm{IL}}$ |  | -0.3 to 0.7 | V |

Electrical Characteristics at $\mathrm{Ta}=25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=\mathrm{VS}=5 \mathrm{~V}$

| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| $\mathrm{V}_{\mathrm{CC}}$ system power supply current | ${ }^{1} \mathrm{CCO}$ | IN 1 to $\mathrm{IN} 8=0 \mathrm{~V}$ |  | 0.1 | 5 | $\mu \mathrm{A}$ |
|  | ${ }^{1} \mathrm{CC}{ }^{1}$ | $\mathrm{IN} 1=\mathrm{IN} 3=3 \mathrm{~V}$ |  | 10 | 16 | mA |
| VS system power supply current | IS0 | IN 1 to $\mathrm{IN} 8=0 \mathrm{~V}$ |  | 0.1 | 5 | $\mu \mathrm{A}$ |
|  | IS1 | $\mathrm{IN} 1=\mathrm{IN} 3=3 \mathrm{~V}$ |  | 13 | 19 | mA |
| Output saturation voltage | $\mathrm{V}_{\text {OUT }}{ }^{1}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{VS}=3 \mathrm{~V} \text { to } 7.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{IN}}=3 \mathrm{~V} \text { or } 0 \mathrm{~V}, \\ & \mathrm{I}_{\mathrm{OUT}}=200 \mathrm{~mA} \text { (High and low side) } \end{aligned}$ |  | 0.3 | 0.4 | V |
|  | $\mathrm{V}_{\text {OUT }}{ }^{2}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{VS}=4 \mathrm{~V} \text { to } 7.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{IN}}=3 \mathrm{~V} \text { or } 0 \mathrm{~V}, \\ & \mathrm{I}_{\mathrm{OUT}}=400 \mathrm{~mA} \text { (High and low side) } \end{aligned}$ |  | 0.6 | 0.8 | V |
| Input current | In | $\mathrm{V}_{\text {IN }}=5 \mathrm{~V}$ |  | 150 | 200 | $\mu \mathrm{A}$ |
| Spark killer diode |  |  |  |  |  |  |
| Reverse current | IS(leak) |  |  |  | 30 | $\mu \mathrm{A}$ |
| Forward voltage | VSF | ${ }^{\text {OUUT }}=400 \mathrm{~mA}$ |  |  | 1.7 | V |

## Package Dimensions

unit:mm (typ)

3260A


Pd max - Ta


## Pin Assignment



## Block Diagram



Top view

Truth Table


L : low, H : high
*: The output logic mode when $\operatorname{IN} 1 / / \mathrm{N} 2=\mathrm{H} / \mathrm{H}$ is determined by the immediately preceding $\mathrm{IN} 1 / / \mathrm{N} 2$ mode.
The post-switching output modes will be as follows.
When switching from (2): (4)
When switching from (3): (4)'
When switching from (1): Undefined (Either (4) or (4)')
The modes when IN3/IN4 = H/H operate similarly as described below.
When switching from (6): (8)
When switching from (7): (8)'
When switching from (5): Undefined (Either (8) or (8)')


VO(sat) - Ta


IS - VS


Vo(sat) - IO


Is - Ta


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